

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Thereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	03/15/05	Name of Person Making the Deposit:	Julie Williams	Signature of the Person Making the Deposit:
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In re Application of: David Kuei

Application No.: 10/769,140

Examiner: Ngan V. Ngo

Filed: 01/29/04

Art Unit: 2818

Confirmation No.: 8276

For: FRACTIONAL BIASING OF SEMICONDUCTORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT TRANSMITTAL

1. Transmitted herewith is an amendment for this application

Transmitted herewith is a response to an office action for the above identified patent application.
(..... 5 sheets)

Transmitted herewith are sheets of substitute formal drawings.

Other:

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

(a) [] Applicant petitions for an extension of time under 37 C.F.R. 1.136
(fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

<u>Extension</u>	<u>Fee</u>
<input type="checkbox"/> one month	\$120.00
<input type="checkbox"/> two months	\$450.00
<input type="checkbox"/> three months	\$1,020.00
<input type="checkbox"/> four months	\$1,590.00
<input type="checkbox"/> five months	\$2,160.00
	<u>Fee \$</u>

If an additional extension of time is required, please consider this a petition therefor.

(b) [] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

Fee Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	12	- 35 =	0	x \$50.00	\$0.00
Independent Claims	2	- 6 =	0	x \$200.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$360.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

5. The full fee due in connection with this communication is provided as follows:

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085. A duplicate copy of this authorization is enclosed.

A check in the amount of \$

Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
 Two North Market Street, Third Floor
 San Jose, California 95113
 (408) 938-9060
 Customer No: 45590

Respectfully submitted,

Date: 3/15/2005

By: 
 Anthony C. Murabito
 Reg. No. 35,295



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Kuei

Serial: 10/769,140 Group Art Unit: 2818

Filed: January 29, 2004 Examiner: Ngan V. Ngo

For: FRACTIONAL BIASING OF SEMICONDUCTORS

Honorable Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT and AMENDMENTS

Dear Sir:

In the Office Action mailed February 17, 2005, the Examiner has stated that the present Application contains two distinct inventions. As such, the Examiner has required the Applicant to elect a single invention for prosecution on the merits. Specifically, the Examiner has required the Applicant to elect between a first invention, Group I, recited in Claims 1-7 and 13-28, drawn to semiconductor device, classified in class 257, subclass 618, and a second invention, Group II, recited in Claims 8-12 and 29-35, drawn to a process for using a semiconductor device, classified in class 327, subclass 1+.

ELECTION WITHOUT TRAVERSE BETWEEN
GROUP I AND GROUP II

Applicants elect without traverse Group II, recited in Claims 8-12 and 29-35, drawn to a process for using a semiconductor device, classified in class 327, subclass 1+.